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Vs $- + R_1$ Ulaby & Maharbiz, Problem 2.23 (1st, 2nd and 3rd edition) I1 I2 R2 RN IN ... 7. Consider the circuits shown in the

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upper right. a. For circuit (a), show that I_1 and I_2 can be written as: $-1 \begin{pmatrix} R_{eq} \\ R_{eq} \end{pmatrix} \begin{pmatrix} 1 & 1 \end{pmatrix} I_1 = \begin{vmatrix} | & | \\ I_s & I_s \end{vmatrix}$ & $I_2 = \begin{vmatrix} R & | \\ | & I_s \end{vmatrix} \dots$

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