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Universal Serial Bus System Architecture

USB (Universal Serial Bus) is widely protocol with different versions and speeds. A maximum of 127 peripherals can be connected to a single USB host controller. USB acts as "plug and play" device. The USB are used in almost devices such as keyboards, printers, media devices, cameras, scanners and mouse.

Serial Communication Protocols: Basics, Transmission Modes ...

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Spansion Redirect - Cypress

```
serial: omninet: add device id for Zyxel Omni 56K Plus commit. serial: option: add Telit LE910-S1
compositions 0x7010, 0x7011 commit. serial: pl2303: add device id for ADLINK ND-6530 GC
commit. serial: ti_usb_3410_5052: add startech.com device id commit. serial: ti_usb_3410_5052:
drop closing_wait module parameter commit
```

LinuxChanges - Linux Kernel Newbies

PA-RISC is an instruction set architecture (ISA) developed by Hewlett-Packard.As the name implies, it is a reduced instruction set computer (RISC) architecture, where the PA stands for Precision

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Architecture. The design is also referred to as HP/PA for Hewlett Packard Precision Architecture.. The architecture was introduced on 26 February 1986, when the HP 3000 Series 930 and HP 9000 Model 840 ...

PA-RISC - Wikipedia

Conceptually, the PCI Express bus is a high-speed, serial replacement of the older PCI/PCI-X bus. One of the key differences between the PCI Express bus and the older PCI is the bus topology; PCI uses a shared parallel bus architecture, in which the PCI host and all devices share a common set of address, data and control lines. In contrast, PCI Express is based on point-to-point topology, with ...

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